

DOCKET NO.: 01-B-077C1 (STMI01-00096)
Customer No. 30425

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : SRIKANTH R. MUROOR

For : VOLTAGE CONTROLLED OSCILLATOR CAPABLE OF
LINEAR OPERATION AT VERY LOW FREQUENCIES

MAIL STOP PATENT APPLICATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement. This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The patents, publications and other information herein are listed below and on the attached Form PTO/SB/08. In accordance with 37 C.F.R. § 1.98(d), these references were previously cited by or submitted to, the Office in parent patent application Serial No. 10/060,739, and therefore copies of the cited references are not required.

U.S. Patents

<u>U.S. Patent No.</u>	<u>Inventor</u>	<u>Date</u>
5,416,437	Contreras, et al.	May, 1995
5,881,014	Ooishi	March, 1999
6,054,887	Horie, et al.	April, 2000

Publications

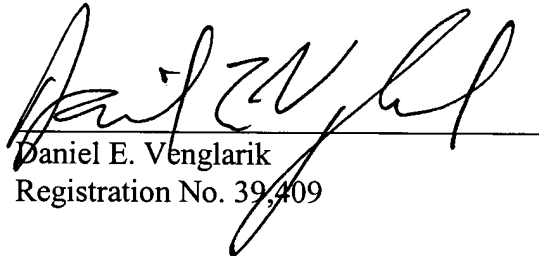
GREGORIAN, R., et al.; "ANALOG MOS INTEGRATED CIRCUITS FOR SIGNAL PROCESSING"; Wiley-Interscience Publication; pages 448-451

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above Patents and to question the relevance and materiality of the Patents and Publications listed herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure Statement.

Respectfully submitted,

DAVIS MUNCK, P.C.

Date: 1-21-04


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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
Application Number					
Filing Date					
First Named Inventor				Srikanth R. Muroor	
Art Unit					
Examiner Name					
Attorney Docket Number				01-B-077C1 (STMI01-00096)	
Sheet	1	of	2		

[illegible][illegible]

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). ² See Kind Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Citation if Known

Application Number

Filing Date

First Named Inventor

Srikanth R. Murrar

Art Unit

Examiner Name

Sheet

2

of

2

Attorney Docket Number

01-B-077C1 (STMI01-00096)

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	BA	GREGORIAN, R., et al.; "ANALOG MOS INTEGRATED CIRCUITS FOR SIGNAL PROCESSING"; Wiley-Interscience Publication, pages 448-451	

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